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## \*\* CONTINUING DATA \*\*\*\*\*

*None*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-096076 03/31/2003

*Verified Jr*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>John C. Jr</i> Examiner's Signature	JAPAN	11	18	3

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## TITLE

Reversible logic elements operating in a synchronous mode

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